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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/670,557	09/26/2003	Yoshihiko Nemoto	65781-015	8124	
7590 04/07/2005			EXAM	MINER	
McDermott, Will & Emery			ROSE, KIESHA L		
600 13th Street, N.W. Washington, DC 20005-3096			ART UNIT	PAPER NUMBER	
			2822	2822	
		DATE MAILED: 04/07/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

·		Application No.	Applicant(s)				
Office Action Summary		10/670,557	NEMOTO ET AL.				
		Examiner	Art Unit				
		Kiesha L. Rose	2822				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)[Responsive to communication(s) filed on	<u>_</u> .					
2a) <u></u>		s action is non-final.					
3)⊠	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4)⊠ Claim(s) <u>1-20</u> is/are pending in the application. 4a) Of the above claim(s) <u>15-20</u> is/are withdrawn from consideration.							
5) Claim(s) 1-14 is/are allowed.							
	6) Claim(s) is/are rejected.						
	7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.							
Applicati	on Papers						
9) The specification is objected to by the Examiner.							
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s)							
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)							
3) 🛛 Inform	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date <u>1/26/04</u> .	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te atent Application (PTO-152)				

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DETAILED ACTION

This Office Action is in response to the election filed 28 December 2004.

Election/Restrictions

Claims 15-20 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected method of making a semiconductor device, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 28 December 2004.

Applicant's election without traverse of claims 1-14 in the reply filed on 28 December 2004 is acknowledged.

Claims 15-20 should be cancelled.

Priority

Receipt is acknowledged of papers filed on 26 September 2003 purporting to comply with the requirements of 35 U.S.C. 119(a)-(d) and they have been placed of record in the file. Attention is directed to the fact that the date for which foreign priority is claimed is not the date of the first filed foreign application acknowledged in the oath or declaration.

Oath/Declaration

Receipt is acknowledged of papers filed under 35 U.S.C. 119 (a)-(d) based on an

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application filed in Japan on 29 November 2003. Applicant has not complied with the requirements of 37 CFR 1.63(c), since the oath, declaration or application data sheet does not acknowledge the filing of any foreign application. A new oath, declaration or application data sheet is required in the body of which the present application should be identified by application number and filing date. The foreign priority claimed in oath is dated November 29, 2003 but a copy of foreign priority papers in the filing of the application is dated November 29, 2002. It is unclear what is the correct date of the foreign priority application.

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested:

Title

Semiconductor Device

Allowable Subject Matter

Claims 1-14 are allowed.

The following is an examiner's statement of reasons for allowance: Claims 1-14 are allowable because prior art does not show alone or in combination along with the limitations of the independent claims such as a first and second semiconductor substrate, a first semiconductor element formed in opposite surface of first substrate

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and comprised of first semiconductor circuit and first electrode, a second semiconductor element formed in opposite surface of second substrate and comprised of second circuit and second electrode, a first wiring conductor formed of electrically conductive material and interposed between first and second electrode and a through electrode extending through first and second substrate and connected to the first and second electrode through the medium of first wiring conductor layer.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

This application is in condition for allowance except for the following formal matters:

The foreign priority date and reference to the foreign priority in the oath/declaration.

Prosecution on the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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Poo et al. (U.S. Patent 6,611,052) discloses a semiconductor device that contains a first semiconductor element for comprised of first semiconductor circuit and first electrode, a second semiconductor element comprised of second circuit and second electrode, a first wiring conductor formed of electrically conductive material and interposed between first and second electrode.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 571-272-1844. The examiner can normally be reached on M-F 8:30-6:00 off 2nd Mondays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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AMIR ZACARIAN

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